

AMENDMENTS TO THE CLAIMS

Claim 1. (Previously Presented) An image display apparatus, comprising:

an image signal processing circuit receiving an image signal and processing the image signal for display as an image;

an image display unit receiving the image signal processed by the image signal processing circuit, and displaying the processed image signal as an image on a screen; and

a control circuit receiving said image signal from said image signal processing circuit and varying a frequency characteristic of the image signal in a periodic manner,

wherein said control circuit includes a coil having a primary winding and a secondary winding, and passes said image signal through said primary winding while controlling current passing through said secondary winding to vary an inductance value of said primary winding in said periodic manner, thereby varying said frequency characteristic.

Claim 2. (Previously Presented) The image display apparatus of claim 1, wherein the image is divided into spatial lines and temporal frames, and the control circuit alters said frequency characteristic once per spatial line in each temporal frame.

Claim 3. (Previously Presented) The image display apparatus of claim 2, wherein the control circuit also alters said frequency characteristic once per said temporal frame in each spatial line.

Claim 4. (Previously Presented) The image display apparatus of claim 1, wherein the control circuit comprises a timing circuit receiving a first synchronizing signal indicating said spatial lines and a second synchronizing indicating said temporal frames, and generating a timing signal by dividing a frequency of the first synchronizing signal, toggling the timing signal once per said spatial line and reversing a phase of the timing signal once per said temporal frame, said frequency characteristic being controlled according to the timing signal.

Claim 5. (Canceled)

Claim 6. (Previously Presented) The image display apparatus of claim 1, wherein the control circuit alternately opens and closes the secondary winding in said periodic manner.

Claim 7. (Previously Presented) An image display apparatus, comprising:

- an image signal processing circuit receiving an image signal and processing the image signal for display as an image;
- an image display unit receiving the image signal processed by the image signal processing circuit, and displaying the processed image signal as an image on a screen; and
- a control circuit varying a waveform characteristic of the image signal in a periodic manner,

wherein said waveform characteristic is an amplitude characteristic, and the control circuit comprises:

a first amplifier circuit amplifying the image signal with a first gain characteristic;
a second amplifier circuit amplifying the image signal with a second gain characteristic differing from the first gain characteristic; and
a timing circuit selecting the first amplifier circuit and the second amplifier circuit alternately.

Claim 8. (Original) The image display apparatus of claim 7, wherein the second amplifier circuit includes a frequency compensation network causing the second gain characteristic to differ from the first gain characteristic at certain frequencies.

Claim 9. (Previously Presented) An image display apparatus, comprising:
an image signal processing circuit receiving an image signal and processing the image signal for display as an image;
an image display unit receiving the image signal processed by the image signal processing circuit, and displaying the processed image signal as an image on a screen; and
a control circuit varying a waveform characteristic of the image signal in a periodic manner,
wherein said waveform characteristic is a timing characteristic, and the control circuit comprises:
a first amplifier circuit amplifying the image signal;
a delay line delaying the image signal;

a second amplifier circuit coupled to the delay line, amplifying the delayed image signal;
and

a timing circuit selecting the first amplifier circuit and the second amplifier circuit alternately.

Claim 10. (Previously Presented) An image display apparatus comprising:

an image signal processing circuit receiving an image signal and processing the image signal for display as an image;

an image display unit receiving the image signal processed by the image signal processing circuit, and displaying the processed image signal as an image on a screen; and

a control circuit receiving said image signal from said image signal processing circuit and varying a waveform characteristic of the image signal in a periodic manner,

further comprising a control unit that determines a resolution of the image signal and activates the control circuit, when said resolution is higher than a predetermined value and does not activate the control circuit when said resolution is lower than the predetermined value.

Claim 11. (Cancelled)

Claims 12 -15. (Canceled)

Claim 16. (Previously Presented) A method of processing an image signal for display as an image by an image display unit, comprising the step of:

periodically varying a waveform characteristic of the image signal,

wherein said step of periodically varying further comprises the steps of:

amplifying the image signal with a first gain characteristic to generate a first amplified signal;

amplifying the image signal with a second gain characteristic, differing from the first gain characteristic, to generate a second amplified signal; and

selecting the first amplified signal and the second amplified signal alternately.

Claim 17. (Cancelled)

Claim 18. (Previously Presented) A method of processing an image signal for display as an image by an image display unit, comprising the steps of:

periodically varying a waveform characteristic of the image signal by acting directly on said image signal,

further comprising the step of determining a resolution of the image signal, said step of periodically varying being performed depending on the resolution,

wherein the step of periodically varying said waveform characteristic is performed when said resolution is higher than a predetermined value and is not performed when said resolution is lower than the predetermined value.

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Claim 19. (Cancelled)